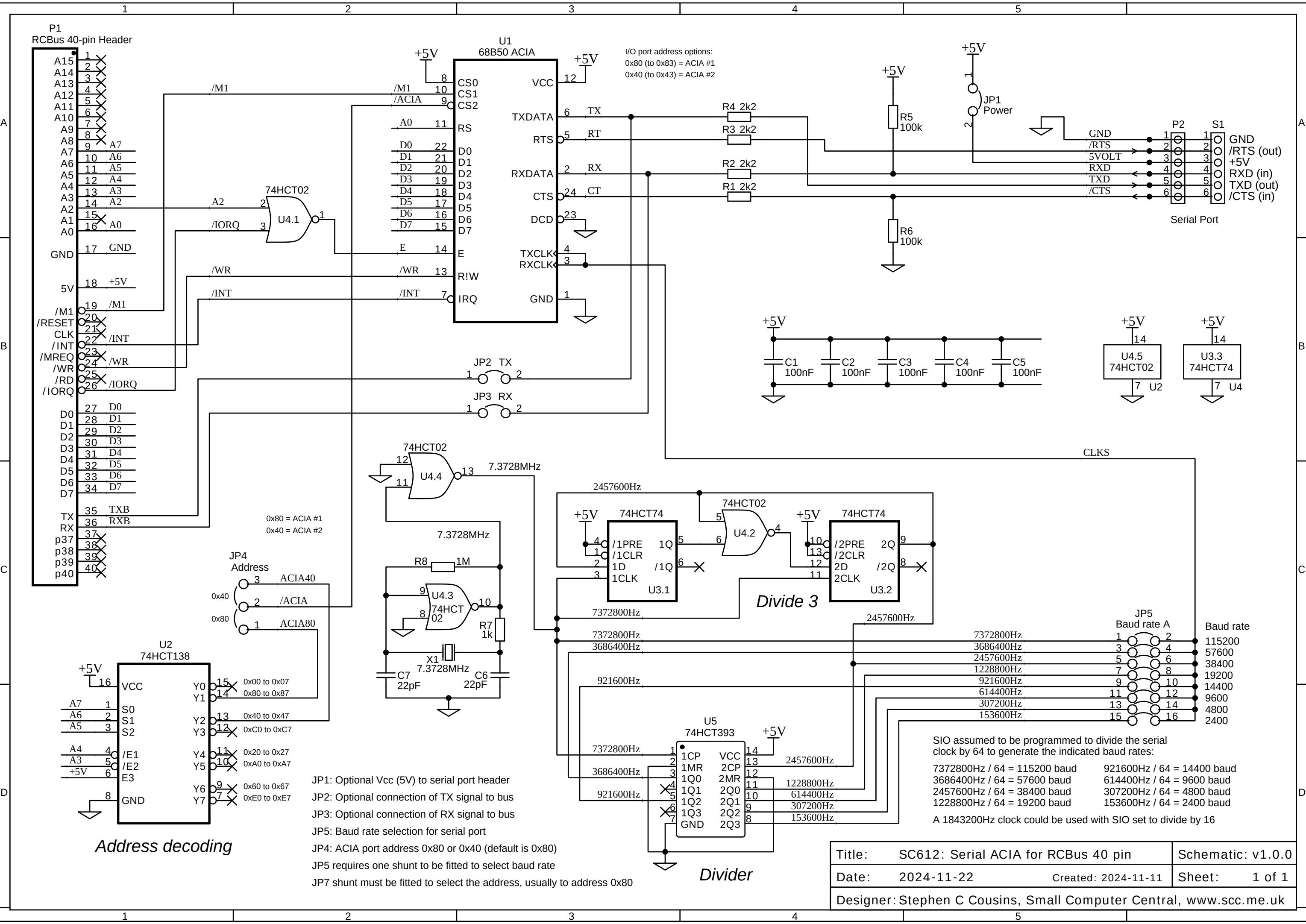




I/O port address options:
0x80 (to 0x83) = ACIA #1
0x40 (to 0x43) = ACIA #2

0x80 = ACIA #1
0x40 = ACIA #2

SIO assumed to be programmed to divide the serial clock by 64 to generate the indicated baud rates:
7372800Hz / 64 = 115200 baud
3686400Hz / 64 = 57600 baud
2457600Hz / 64 = 38400 baud
1228800Hz / 64 = 19200 baud
921600Hz / 64 = 14400 baud
614400Hz / 64 = 9600 baud
307200Hz / 64 = 4800 baud
153600Hz / 64 = 2400 baud
A 1843200Hz clock could be used with SIO set to divide by 16

Title: SC612: Serial ACIA for RCBus 40 pin		Schematic: v1.0.0	
Date: 2024-11-22		Created: 2024-11-11	
Designer: Stephen C Cousins, Small Computer Central, www.scc.me.uk		Sheet: 1 of 1	