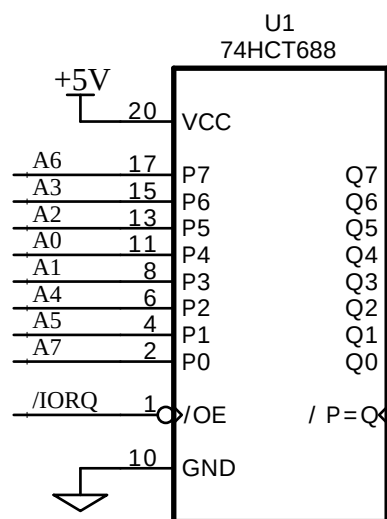
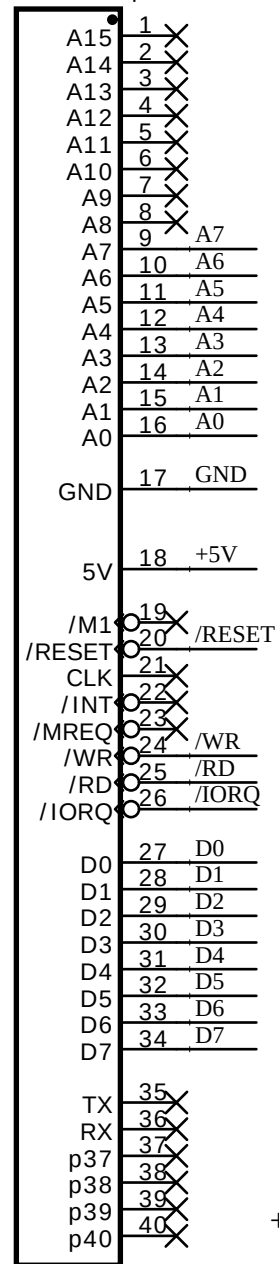
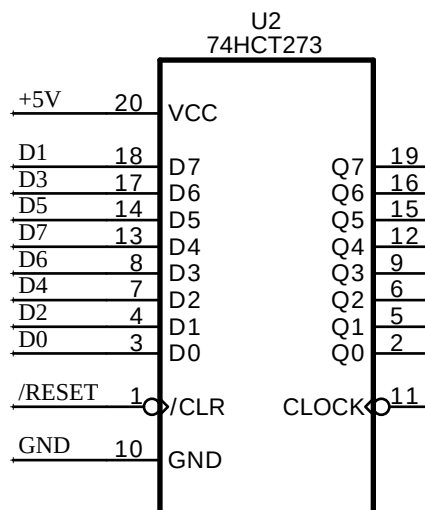
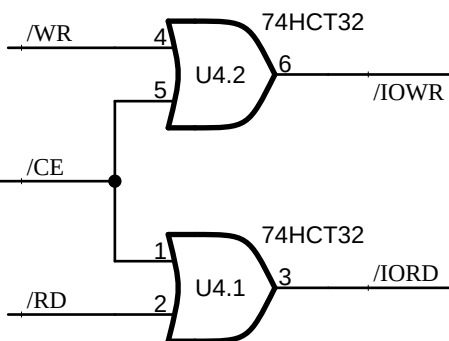
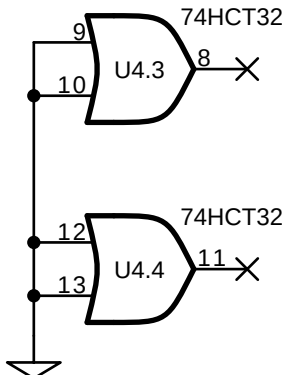
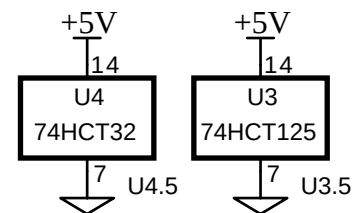
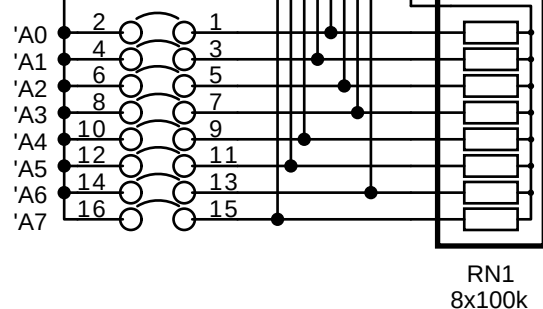


P1
RCBus 40-pin Header



Address decoding

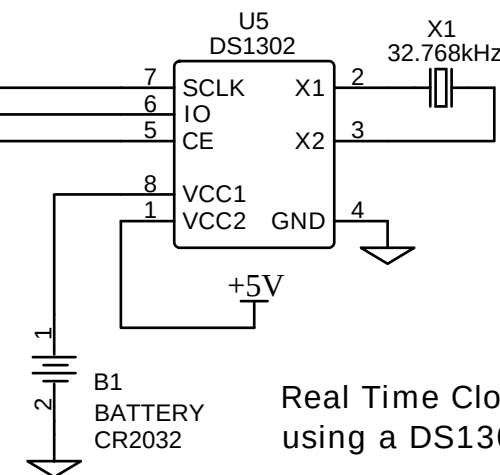
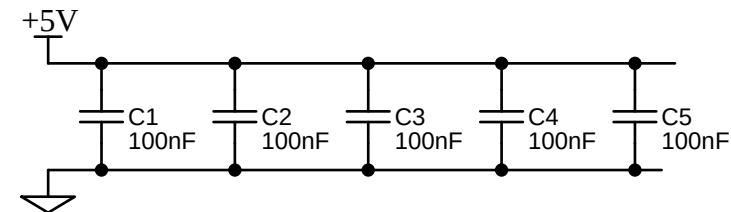
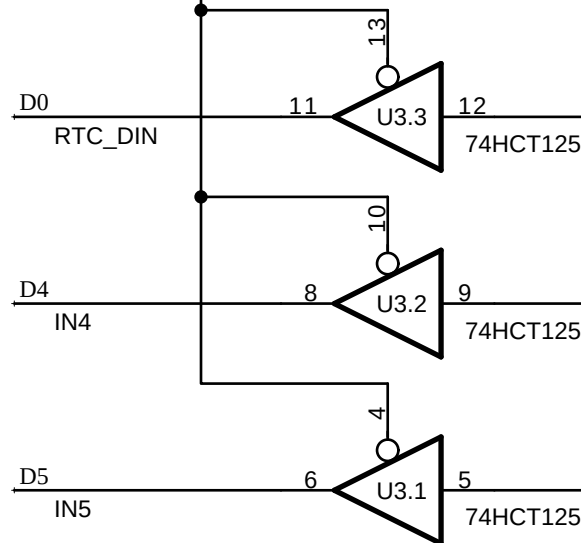
JP1
Address
jumpers



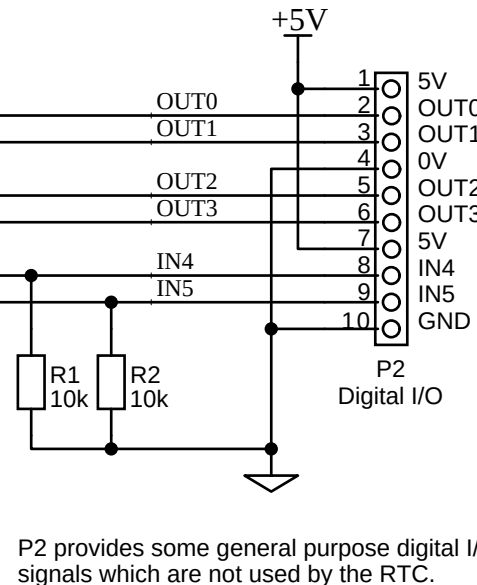
Output bits
D7 -> RTC_DOUT
D6 -> RTC_SCLK
D5 -> RTC_WE
D4 -> RTC_CE

D3 -> OUT3
D2 -> OUT2
D1 -> OUT1
D0 -> OUT0

Input bits
D0 -> RTC_DIN
D4 -> IN4
D5 -> IN5



Real Time Clock
using a DS1302



P2 provides some general purpose digital I/O
signals which are not used by the RTC.

JP1 allows the module to be set to any single I/O address from 0 to 255 (0x00 to 0xFF).
Fit jumper shunt(s) to bit position(s) 0 to 7 to set the required address.
When a jumper shunt is fitted the address bit must be a 1 (high) to enable this module.

Title:	SC606: RTC module for RCBus 40 pin	Schematic:	v1.0.0
Date:	2024-09-12	Created:	2024-09-08
Designer:	Stephen C Cousins, Small Computer Central, www.scc.me.uk	Sheet:	1 of 1