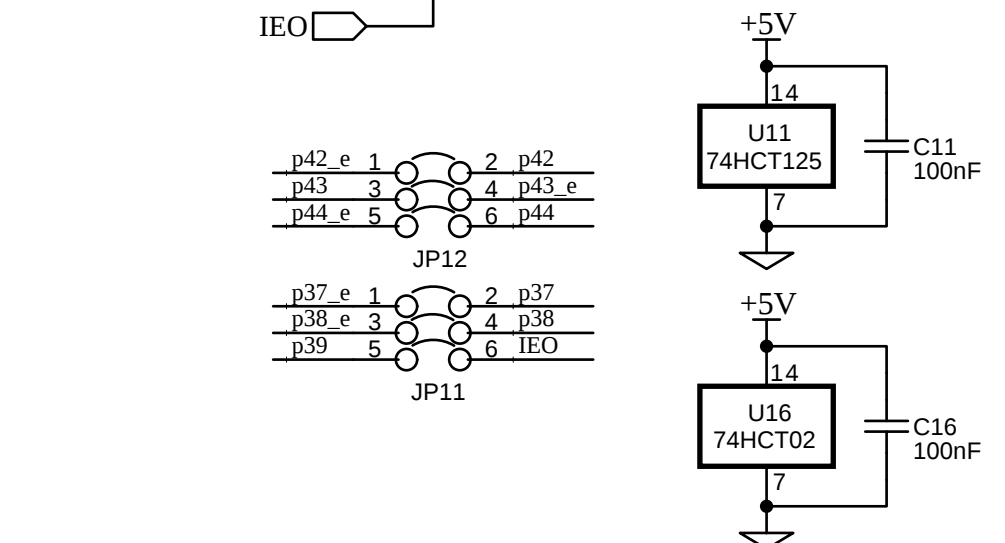
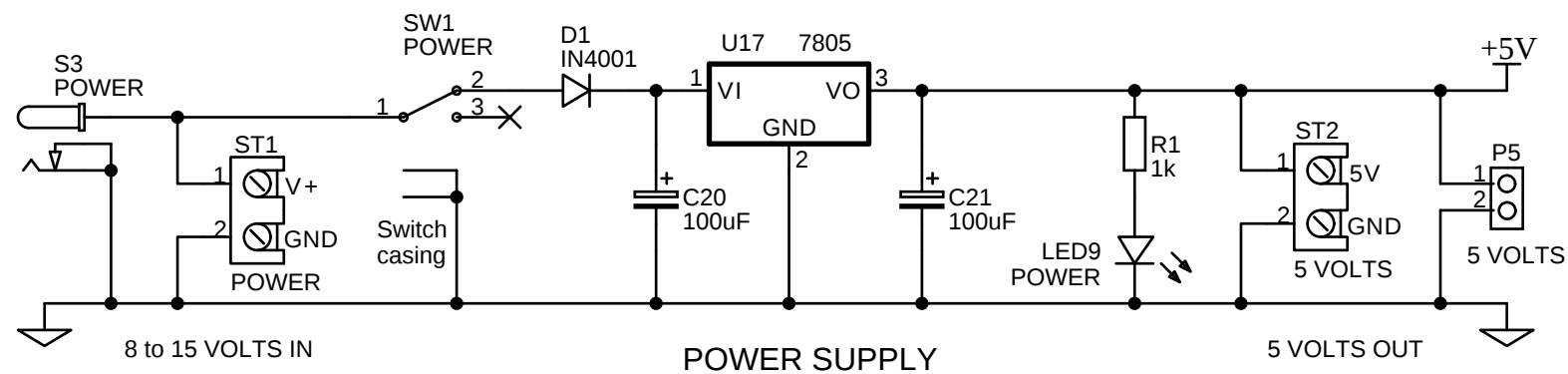
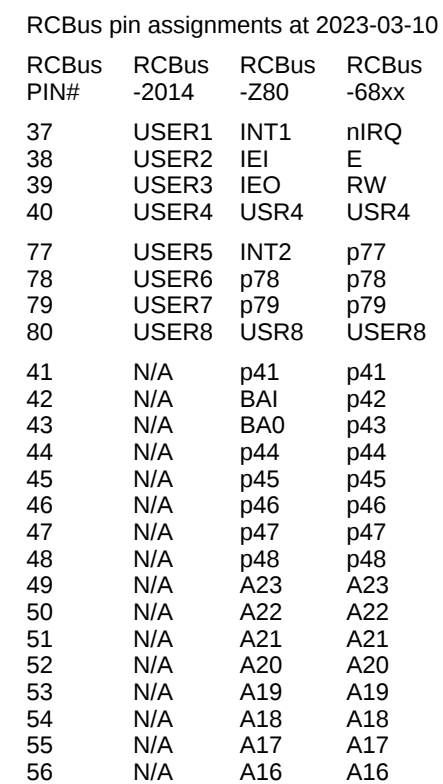
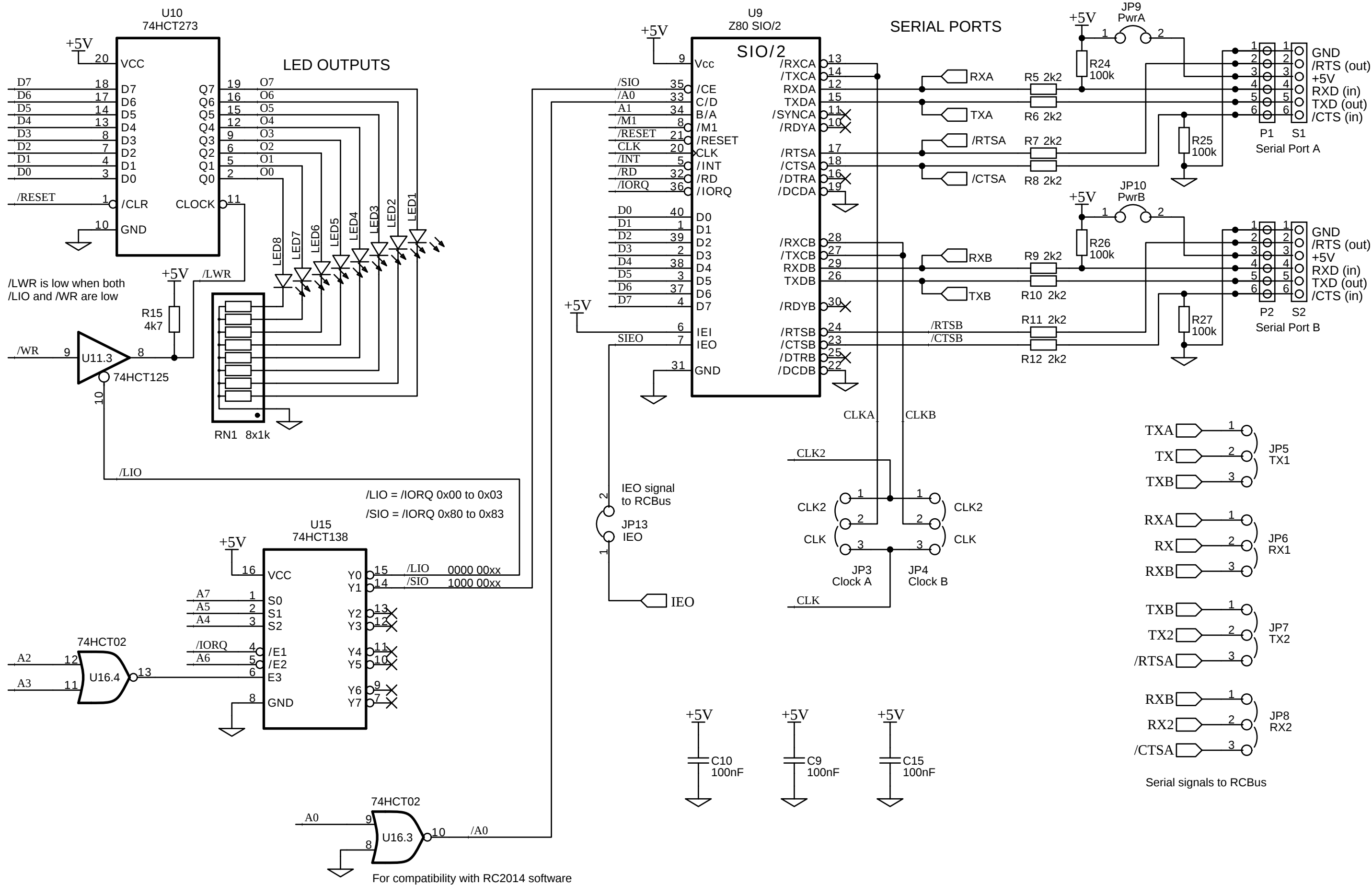
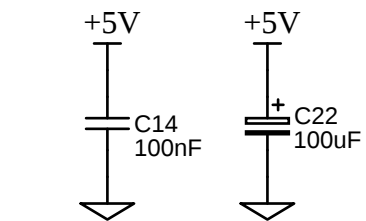




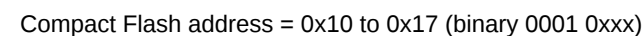
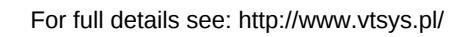
Title:	SC720: Z80 SBC - CPU, Clock, Reset	Schematic:	v1.0.0
Date:	2023-06-08	Created:	2023-05-22
		Sheet:	1 of 4
Designer: Stephen C Cousins, Small Computer Central, www.scc.me.uk			



Title:	SC720: Z80 SBC - Input/output	Schematic: v1.0.0
Date:	2023-06-08	Created: 2023-05-22
Sheet:	3 of 4	
Designer: Stephen C Cousins, Small Computer Central, www.scc.me.uk		



U13.3, U13.4 and U12.1 delay the Read signal



Title:	SC720 Z80 SBC - Compact Flash		Schematic: v1.0.0	
Date:	2023-06-08	Created:	2023-05-22	Sheet: 4 of 4
Designer: Stephen C Cousins, Small Computer Central, www.scc.me.uk				