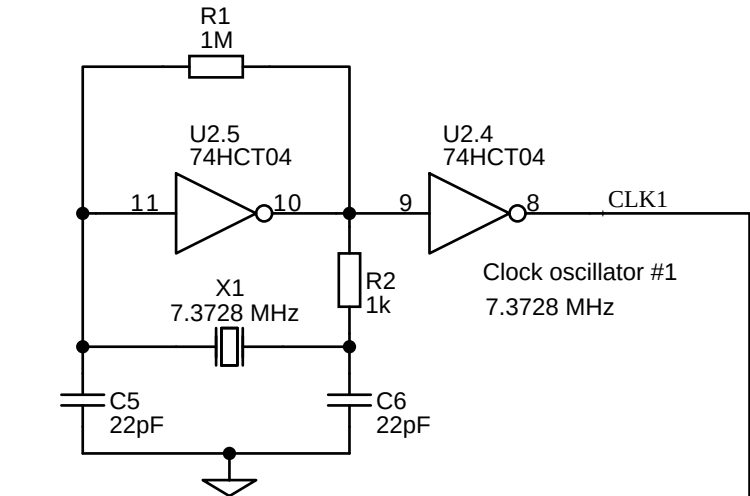
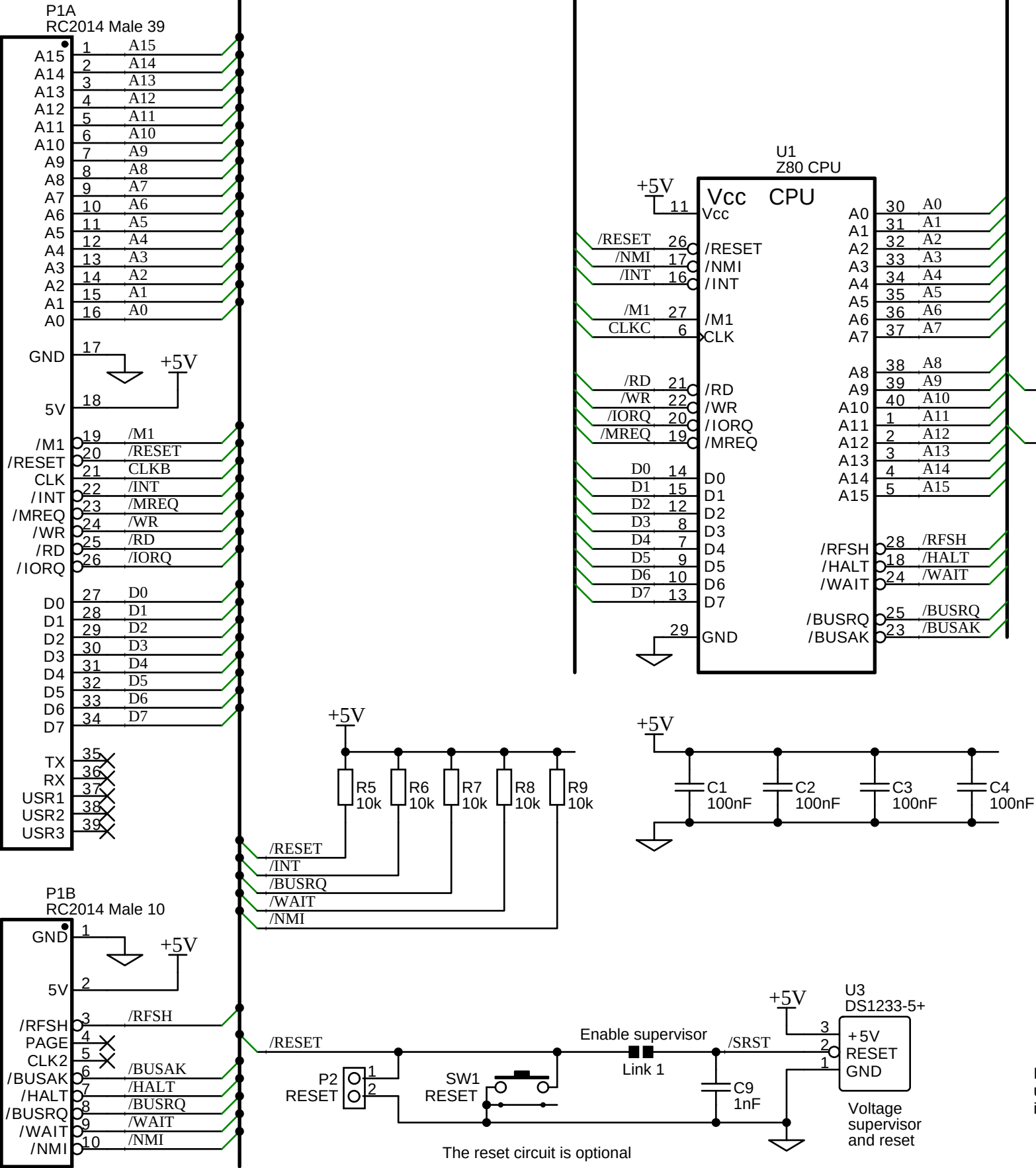
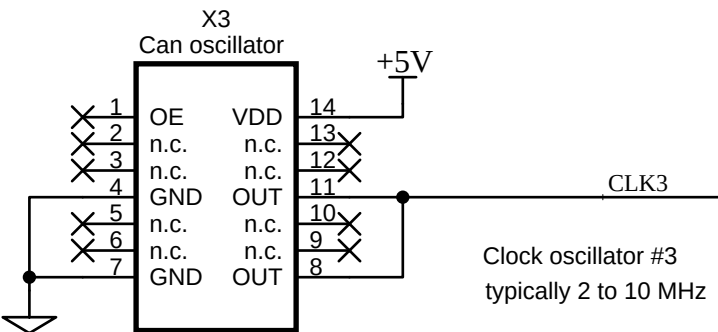


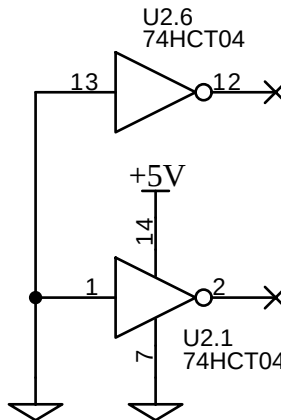
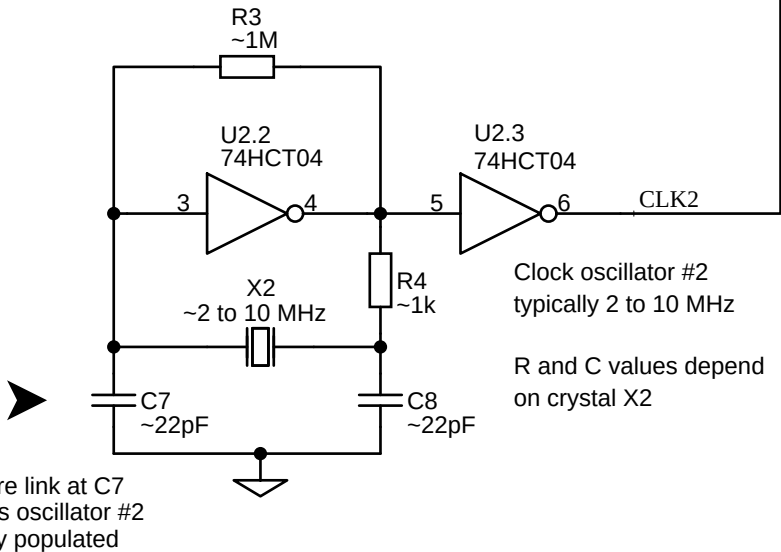
CPU clock = CLK1 *
CPU clock = CLK2
CPU clock = CLK3
Bus clock = CPU clock *

* Default jumper positions



Only one oscillator is required
The other two are optional

This schematic assumes
X1 is a 7.3728 Mhz



Fit wire link at C7
unless oscillator #2
is fully populated

The reset circuit is optional

Link 1 is formed by two solder pads
Bridge with solder to connect the voltage supervisor
Only one supervisor should be connected to the reset line

Title:	SC153: Z80 CPU module for RC2014	Schematic:	v1.1.1
Date:	2023-06-18	Created:	2022-12-20
Designer:	Stephen C Cousins, Small Computer Central, www.scc.me.uk	Sheet:	1 of 1