# Modular Backplane For RC2014 User Guide

For module: SC113 version 1.1

Design and Documentation by Stephen C Cousins

Edition 1.1.1

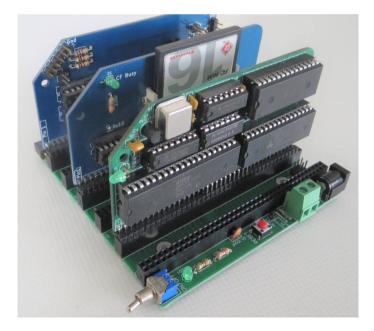
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## **Overview**

My goal is to produce a number of backplane **sections** which can be joined together to form a larger backplane, or used individually for small systems. Not only can the backplane be of variable length, but it can be made of sections that have any special features required. It could even allow cards with different types of bus connector to be used together.





This backplane section has six RC2014 (80 pin) card sockets, an 80 pin expansion plug and an 80 pin expansion socket. Jumpers allow some signals to be isolated or connected between module sockets.



Pin 40 of the standard RC2014 bus is used for the Z80 interrupt daisy chain signal IEO, with the 40<sup>th</sup> pin of the enhanced bus, sometimes called pin 80, being used for the IEI signal. These two pins therefore do not connect to all 6 sockets in parallel, but instead link pin 40 of one socket to pin 80 of the next, forming a chain.

# **Printed Circuit Board**

The printed circuit board is 4 inches by 4 inches (101.6 mm by 101.6 mm).

Printed circuit board, top/component side:

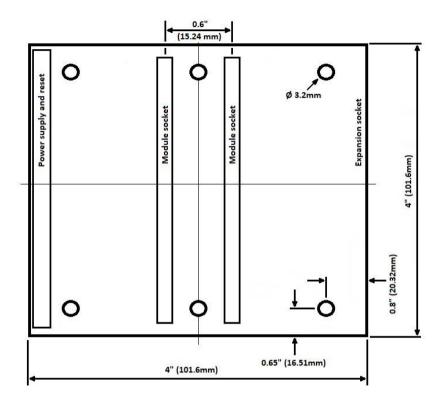
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The circuit board's layout and dimensions are shown below.

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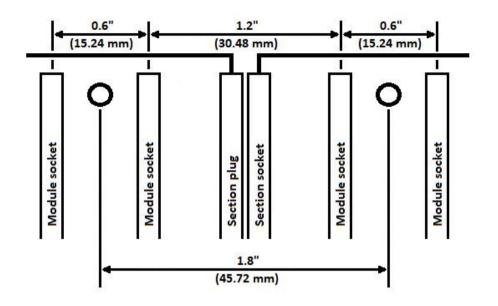


There are six fixing holes, symmetrically placed.

This backplane section has six RC2014/80 module sockets, 0.6" apart, and centred on the circuit board.

Jumpers are provided to enable some bus signals to be isolated or connected to adjacent module sockets.

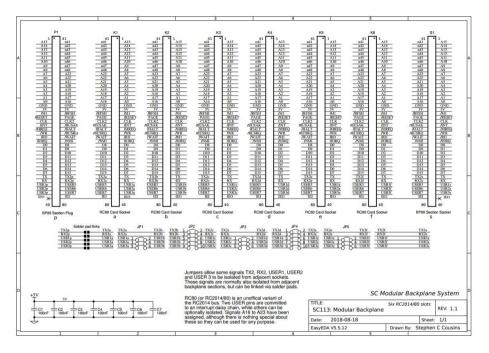
The spacing between two joined sections is shown below.



This spacing is for modular backplane sections that are connected with through hole connectors, such as SC112, not the alternative design using edge mounted connectors.

For further information about the Modular Backplane system consult the document "Modular Backplane Specification" at <u>www.scc.me.uk</u>

# **Schematic**



A high quality version of the schematic is available as a separate PDF.

#### Errata

None yet.

# What You Need

The following components are required to assemble the module with its full functionality and jumper options. Header pins JP1 to JP5 may need to be cut from longer strips.

Image	Qty	Reference	Description
	1	РСВ	Printed circuit board SC113 Backplane with PSU
	7	C1 to C7	Capacitor 100nF, ceramic, lead spacing = 2.54mm
-	3	JP1, JP3 and JP5	Pin header, male, 2 rows x 3 pins, straight
*	2	JP2 and JP4	Pin header, male, 2 rows x 5 pins, straight
	19	JP1 - JP5 shunt	Jumper shunt for pin spacing = 2.54mm (unlikely to need all 19)
In the second seco	1	P1	Pin header, male, 2 row x 40 pins, angled
<u>hhbbhbbb</u>	1	S1	Pin header, socket, 2 rows x 40 pins, angled
	6	K1 to K6	Pin header, socket, 2 rows x 40 pins, straight

Also required to assemble the module from the above components:

- Long nose pliers
- Side cutters
- Soldering iron
- Solder
- PCB cleaning materials

# **Components: What They Do & Where To Get Them**

Each component is described below. I have listed multiple sources for most components, but have not actually tried all of them, so best treat the specified part numbers as guidance only. Where eBay is listed as a supplier, the part is likely to be cheaper there than the other sources, sometimes considerably cheaper. Further savings are usually possible by ordering parts direct from countries like China.

#### PCB

Image	Qty	Reference	Description
	1	PCB	Printed circuit board SC113 Backplane
		Supplier	Part number
		EasyEDA	Search EasyEDA.com for RC2014 Backplane
		EasyEDA	Search Tindie.com for SC113

The PCB is currently only available to be ordered from EasyEDA.com and Tindie.com, although you can download the Gerber and send it to your preferred manufacturer.

#### C1 to C7

Image	Qty	Reference	Description
	7	C1 to C7	Capacitor 100nF, ceramic, lead spacing = 2.54mm
		Supplier	Part number
		Farnell	1100533
		Mouser	75-1C10Z5U104M050R
		RS	699-5027

These capacitors provide power supply decoupling (or bypass). The fast switching in digital circuits creates spikes on the power supply lines which are suppressed with decoupling capacitors placed at key points on the circuit board.

## JP1, JP3 and JP5

Image	Qty	Reference	Description
×	3	JP1, JP3 and JP5	Pin header, male, 2 rows x 3 pins, straight
		Supplier	Part number
		eBay	200906546562 (2x40 pin to be cut to length)
		Farnell	2356151 (2x40 pin to be cut to length)
		Mouser	710-61308021121 (2x40 pin to be cut to length)
		RS	155-721 (2x40 pin to be cut to length)

These jumpers allow backplane signals USER1, USER2 and USER3 to be either isolated or connected to adjacent sockets.

#### JP2 and JP4

Image	Qty	Reference	Description
2 JP2 at		JP2 and JP4	Pin header, male, 2 rows x 5 pins, straight
		Supplier	Part number
		eBay	200906546562 (2x40 pin to be cut to length)
		Farnell	2356151 (2x40 pin to be cut to length)
		Mouser	710-61308021121 (2x40 pin to be cut to length)
		RS	155-721 (2x40 pin to be cut to length)

As with JP1, JP3 and JP5, these jumpers allow backplane signals USER1, USER2 and USER3 to be either isolated or connected to adjacent sockets. The extra two jumper positions allow TX2 and RX2 to be either isolated or connected to adjacent sockets.

Due to the number of modules now using the signals USER1, USER2, USER3, TX2 and RX2, it may be necessary to isolate some modules from others to prevent conflicts.

In addition, some modules use USER pins to create interrupt daisy chains. In such cases the required pins can be linked with jumper wires (eg. Dupont wires) or by soldering links under the board.

To create a simple straight through backplane, fit jumper shunts in all positions.

Warning: If you have modules with single row RC2014 bus pins, the modules sit lower and can hit these jumpers. If this is a problem then solder wire links instead of fitting these jumper header pins.

## JP1 - JP5 shunts

Image	Qty	Reference	Description
	19	JP1 - JP5 shunt	Jumper shunt for pin spacing = 2.54mm (unlikely to need all 19)
		Supplier	Part number
		eBay	201261690156
		Farnell	2396303
		Mouser	649-68786-102LF
		RS	674-2397

These shunts (small sockets) connect the required pins on JP1 to JP5.

#### **P1**

Image	Qty	Reference	Description
In the second second	1	P1	Pin header, male, 2 row x 40 pins, angled
		Supplier	Part number
		eBay	200906546562
		Farnell	2032912 (very expensive)
		Mouser	571-9-103795-0
		RS	155-743

This connector provides a means to plug in another modular backplane section. If you only want a single 6 slot backplane then there is no need to fit this connector.

#### **S1**

Image	Qty	Reference	Description
1 <sup>51</sup>		S1	Pin header, socket, 2 rows x 40 pins, angled
		Supplier	Part number
		eBay	382468980400 (from China)
		Farnell	1807736 (very expensive)
		Mouser	Could not find this part
		RS	Could not find this part

This connector provides a means to plug in another modular backplane section. If you only want a single 6 slot backplane then there is no need to fit this socket.

## K1 to K6

Image	Qty	Reference	Description
6		K1 to K6	Pin header, socket, 2 rows x 40 pins, straight
		Supplier	Part number
		eBay	200906546562 (from UK)
		eBay	362278235395 (from Hong Kong)
		Farnell	1807410 (very expensive)
		Mouser	200-BCS140LDTE (very expensive)
		RS	765-5859 (very expensive)

These connectors are the sockets into which RC2014 modules are plugged.

# Assembly Guide

This guide assumes you are familiar with assembling circuit boards, soldering and cleaning. If not, it is recommended you read some of the guides on the internet before continuing.

First check you have all the required components, as listed in the section "What You Need". Header pins JP1 to JP5 may need to be cut from longer strips.



Fit and solder capacitors C1 to C7.

These can be fitted either way round, as they are not polarity dependent.

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Fit and solder S1 (shown in yellow below).

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	00		00	JP1		00	0 0 R2	00	JP3	00	0 0 R2	00	JP5	00	00	
	00		00		) UI (	00		00		00	0001	00		00	00	
	00		00	- o c	> U2 (	00	0 O U2	00	000	200	0 0 02	00	- O O U2	00	00	
	00		00	00	03	00	0 O U3	00	000	300	0 O U3	00	0 0 03	00	00	
L	00		00	IEO	EI	00	IEO IEI	00	IEO IE	00	IEO IEI	00	IEO IEI	00	00	- k



Fit and solder K1 to K6.

Г	OC		00	0	00	1) n41	00	n	00	A15	00	1	00	<u>oo</u>	
P	00		00	К1	00	K2 n42	00	K3 K4	00	A14 K5	00	K6	00	00	S1
	00		00		00	n43	00		00	A13	00		00	00	<u> </u>
	00		00		00	n44	00		00	A12	00		00	00	
	00		00		00	n45	00		00	A11	00		00	00	
plug	00		00		00	n46	00		00	A10	00		00	00	cet
9	00		00		00	n47	00		00	A9	00		00	00	Expansion socket
Expansion	00		00		00	n48	00		00	A8	00		00	00	1 S
SUB	00		00		00	A23	00		00	A7	00		00	00	sion
, X	00		00		00	A22	00		00	A6	00		00	00	dns
	00		00		00	A21	00		00	A5	00		00	00	d×.
	00		00		00	A20	00		00	A4	00		00	00	
	00		00		00	A19	00		00	A3	00		00	00	
	QO		00		00	A18	00		00	A2	00		00	00	
	00	ž	00	Ē.	00	417 A17 A16	00	IOUNF 1000F	00	A1 40	00	1	00	00	
	00	Ş	00	2	00		00	5 ¢	00	A0 🗧	00	2	00	00	
	00	4	00	Þ	00	D GND	00	D (	00	GND 🧯	00	6	00	00	
	00	ALL ALIANE	00			GND 5V 3/RFSH	00	ane		GND ( 5V (	00			00	
	00	3	00	3	00		00	u+ L Backplane	00	/M1 g	00	3	00	00	
	00		00		00	PAGE	00	d	00	/RESET	00		00	00	
	00		00		00	CLK2	00	- <del>Ž</del>	00	CLK	00		00	00	
	QO		00		00	/BUSAK	00	B	00	/INT	00		00	00	
	00		00		00	/HALT	00	<u> </u>	00	/MREQ	00		00	00	
	00		00		00	/BUSRQ	00	윽	00	/WR	00		00	00	
	00		00		00	/WAIT	00	Modular	00	/RD	00		00	00	
	QO		00		00	/NMI	00	Ň	00	/IORQ	00		00	00	
	00		00		00	D8	00	<b>.</b>	00	DO	00		00	00	
	00		00		00	D9	00	v1.1	00	D1	00		00	00	
	00		00		00	D10	00	SC113	00	D2	00		00	00	
	00		00		00	D11	00	5	00	D3	00		00	00	
	00		00		00	D12	00	Š	00	D4	00		00	00	
	00		00		00	D13	00		00	D5	00		00	00	
	00	1	00		00	D14	00		00	D6	00		00	00	
	00		00		00	JP2 D15	00		00	D7 JP4	00		00	00	
	00		00		00		00		00		00		00	00	
	00		00	JP1	00	OOR	00	JP3	00		00	JP5	00	00	
	00		00		00	000	00		00		00		00	00	
	00		00	000	00	0.0 0	00	-0.0 •	00	000	00	-0.00	00	00	
	00		00	000	00		00	00	00	000	00	-000	00	00	
	00		00	EO IE	00	EO IEI	00	EO IEI	00	EO IE	00	IEO IEI	00	00	



Fit and solder JP1, JP3 and JP5 (shown in red below).



Fit and solder JP2 and JP4 (shown in yellow below).

Warning: If you have modules with single row RC2014 bus pins, the modules sit lower and can hit these jumpers. If this is a problem then solder wire links instead of fitting these jumper header pins.

	00 1 n41		00 1 A15		
P1 00 00 K1	00 K2 n42	and a state of the	00 A14 K5	00 K6	00 00 <sub>s1</sub>
00 00	00 n43		0 0 A13	00	00 00
00 00	0 0 n44	00	O O A12	00	00 00
00 00	00 n45	00	0 0 A11	00	00 00
	00 n46	00	0 0 A10	00	00 00 <b></b>
	00 n47		00 A9	00	socket
	/OO n48	00	0 0 A8	00	
	O O A23	00	0 0 A7	00	
200 00	0 0 A22	00	0 0 A6	00	00 00 8
00 00	O O A21	00	0 0 A5	00	00 00 \$
00 00	O O A20	00	00 44	00	00 00 "
00 00	O O A19	00	0 0 A3	00	00 00
00 00	0 0 A18	00	0 0 A2	00	00 00
200 <sup>2</sup> 00		100nF		1001F	00 00
	<u>00</u> ₽ A16	and the second se	<b>00</b> A0 🚊	00 <u>Š</u>	00 00
000000	OO O GND	000 0	GND G	00 0	00 00
000000	O O O 5V	000 00			00 00
005008	OO ? /RFSH	Backplane cs C	00 /M1 g	00 5	00 00
00 00	O O PAGE	00 1	OO /RESET	00	00 00
00 00	O O CLK2	00-8	O O CLK	00	00 00
00_00	O O /BUSAK	00 8	OO /INT	00	00 00
00 00	OO /HALT	00	OO /MREQ	00	00 00
00_00	O O /BUSRQ	0000 0000 Modular	OO /WR	00	00 00
00 00		00 - <u>ğ</u>	OO /RD	00	00 00
00 00	O O /NMI	00 ¥	O O /IORQ	00	00 00
00_00	0 O D8	00	00-00	00	00 00
00_00	00 09	00 <del>-</del>	00 01	00	00 00
00_00	0 0 D10	00 M	0 0 D2	00	00 00
00_00	00 D11	000 000 SC113	00 03	00	00 00
00_00	0 0 D12		0 0 D4	00	00 00
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00 00			OO DZ JP4		00 00
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	States of the second se	A REAL PROPERTY AND ADDRESS OF TAXABLE PROPERTY.		O O P5	00 00
		00 00		animum files a literation files	00 00
				00 0 0 2	00 00
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					00 00

Remove any solder 'splats' with a brush, such as an old toothbrush.

Visually inspect the soldering for dry joints and shorts.

Clean the flux off with suitable cleaning materials.

Visually inspect again.

# **Configuring the Backplane**

The only configuration options for the backplane are to connect or isolate adjacent card slots with jumpers on JP1 to JP5.

JP1 to JP5 allow bus signals USER1, USER2 and USER3 to be connected to adjacent sockets, when the jumper shunts are fitted, or isolated from adjacent sockets when the jumper shunts are not fitted. JP2 and JP4 have additional jumper positions for bus signals TX2 and RX2.

The illustration below shows jumper shunt positions, shown in red, to link signal USER2 to all 6 module sockets. It also shows jumper shunt positions, shown in yellow, to connect TX2 and RX2 between module sockets K4 and K5.

	00	Ĩ	00		00	1 n41	00	1	00	1 A15	00		00
P1	00	3	00		00	K2 n42	00	K3 K4	00	A14 K5	00	K6 0 0	00 <sub>S1</sub>
	00	0	00		00	n43	00		00	A13	00	00	00
	oc	3	00		00	n44	00		00	A12	00	00	00
	00	3	00		00	n45	00		00	A11	00	00	00
pn	00	3	00		00	n46	00		00	A10	00	00	00
Expansion plug	00	0	00		00	n47	00		00	A9	00		socket
ior	00		00		00	n48	00		00	A8	00		
ans	00		00		00	A23	00		00	A7	00	00	
đx	O C	3	00		00	A22	00		00	A6	00	00	00
ш	00	0	00		00	A21	00		00	A5	00	00	
	00	3	00		00	A20	00		00	A4	00	00	00
	00	9	00		00	A19	00		00	A3	00	00	00
	00		00		00	A18	00		00	A2	00	00	00
	00	100nF	00	100nF	00	417 410 416	00	100nF 100nF	00	A1 40 00	00	0 0 v	00
	00	100	00	00	00	∯ A16	00	100 100	00	A0 0	00	– ŠOO	00
	00	0	00	0	00	GND GND	00	0 0	00	GND 🚺	00	000	00
	00	0	00	0	00	0 5V	00	0 0 0 0	00	5V 💿	00	000	00
	00	5	00	5	00	No /RFSH	00	c4 [c Backplane c5 [c	00	/M1 8	00	500	00
	oc		00		00	PAGE	00	o Iq	00	/RESET	00	00	00
	00	0	00		00	CLK2	00	<u> </u>	00	CLK	00	00	00
	QC		00		00	/BUSAK	00	ğ	00	/INT	00	00	00
	00	3	00		00	/HALT	00		00	/MREQ	00	00	00
	00	3	00		00	/BUSRQ	00	Modular	00	/WR	00	00	00
	00		00		00	/WAIT	00	- P	00	/RD	00	00	00
	QC		00		00	/NMI	00	No No	00	/IORQ	00	00	00
	00		00		00	D8	00		00	DO	00	00	00
	00	3	00		00	D9	00	÷	00	D1	00	00	00
	00		00		00	D10	00		00	D2	00	00	00
	QC		00		00	D11	00	SC113	00	D3	00	00	00
	00	3	00		00	D12	00	000	00	D4	00	00	00
	00	3	00		00	D13	00		00	D5	00	00	00
	00		00		00	D14	00		00	D6	00	00	00
	Q C	3	00		00	JP2 D15	00		00	D7 JP4	00		00
	00	0	00		00		00		00	0 72	00	00	00
	00	3	00		00	0 0 R2	00	JP3	00	0 O R2	00	JP5 OO	00
	00	0	00		00	000	00		00	0.0 01	00		00
	00		00		00	00 02	00	- O O U2	00	- O O J2	00	00400	00
	00	0	00	000	00	0 0 03	00	0 0 03	00	0 0 03	00	0000000	00
	00		00	EO IEI	00	IEO IEI	00	IEO IEI	00	IEO IEI	00	IEO IEI OO	00

Signals TX2 and RX2 are hard wired between module socket pairs K1 and K2, and K3 and K4, and K5 and K6. Thus with shunts in the positions shown above in yellow, TX2 and RX2 are common to K3, K4, K5 and K6.

Some RC2014 modules use USER2 and USER3 for the interrupt daisy chain signals IEI and IEO.

00	00	1	00	1 n41	00	1		00	1 A15		00	1	0	00	
100	00	К1	00	K2 n42	00	K3	K4	00	A14	K5	00	K6	00	00	s
00	00		00	n43	00			00	A13		00		00	00	
00	00		00	n44	00			00	A12		00		00	00	
00	00		00	n45	00	-		00	A11		00		00	00	
So o	00		00	n46	00			00	A10		00		00	00	
	00		00	n47	00			00	A9		00		00	00	
	00		00	n48	00		/	00	A8		00		00	00	
<i>j</i> oo	00		00	A23	00			00	A7		00		00	00	
200	00		00	A22	00			00	A6		00		00	00	
0.0	00		00	A21	00			00	A5		00		00	00	
00	00		00	A20	00			00	A4		00		00	00	
00	00		00	A19	00			00	A3		00		00	00	
00	00		00	A18	00			00	A2		00		00	00	
00	00	100nF	00	417 A17 A16	00	100nF	100nF	00	A1	100nF	00	JOONF	00	00	
00	200	ŏ	00	∯ A16	00	10		00	AO	ĕ	00	- OF	00	00	
000	00	۲	00	GND	00	۲	0	00	GND	0	00	0	00	00	
000	00	0	00	6V	00	0 0		00	5V		00		00	00	
00	,00	C2	00	S /RFSH	00	c4 [ Backplane	C5	00	/M1	8	00	67	00	00	
00	00	Ŭ	00	PAGE	00			00	/RESET		00		00	00	
00	00		00	CLK2	00	- <del>X</del>		00	CLK		00		00	00	
00	00		00	/BUSAK	00	ğ		00	/INT		00		00	00	
00	00		00	/HALT	00			00	/MREQ		00		00	00	
00	00		00	/BUSRQ	00	lar		00	/WR		00		00	00	
00	00		00	/WAIT	00	p		00	/RD	$ \rightarrow $	00		00	00	
00	00		00	/NMI	00	Modular		00	/IORQ		00		00	00	
00	00		00	D8	00			00	DO		00		00	00	
00	00		00	D9	00	v1:1		00	D1		00		00	00	
00	00		00	D10	00			00	D2		00		00	00	
00	00		00	D11	00	SC113		00	D3		00		00	00	
00	00		00	D12	00	- O		00	D4		00		00	00	
00	00		00	D13	00	0,		00	D5		00		00	00	
00	00		00	D14	00			00	D6		00		00	00	
00	00		00	JP2 D15	00			00	D7 JP	4	00		00	00	
00	00		00		00			00			00		00	00	
00	00	JP1	00	0 0 R2	00	JP:	3	00	00		00	JP5	00	00	
00	00			0.0 UI	00			00	O C		00		00	00	
00	00	-000	aparate and	0 0 U2	00	-0					00	0 0 02	00	00	
00	00	-00	No. of Concession, Name	0 03	00		0 03	00			00	0 03	00	00	
00	-00	IEO IE	Contraction of the local division of the loc	IEO IEI	00	EO	EI EI	00	IEO	EI	00	IEO IEI	00	00	

The illustration below shows links required to implement this daisy chain.

The interrupt daisy chain links the IEI output of one module to the IEO input of the module to its right, as illustrated above. Where USER2 (pin 38) is IEI and USER3 (pin 39) is IEO.

Dupont cables could be used to make these links, although standard cables will likely be rather long. Alternatively wire links could be soldered on the bottom of the boards for a more permanent and tidier solution.

# **Purchasing the Printed Circuit Board**

Currently the circuit board is available from EasyEDA (in China), or more accurately from their production partner JLCPCB.

You can download Gerber files from EasyEDA and send them to your preferred manufacturer, but the following describes the ordering process through EasyEDA.

Browse to EasyEDA.com

Select the main menu item "Explore"

In the search box, enter "RC2014 SC113" or "sccousins"

Select, from the list shown, the project "SC113 v1.x Backplane"

The project's details should now be displayed.

Select "Open in Editor" (the button next to the circuit board illustration, not the one next to the schematic).

Select "Generate Fabrication File (Gerber)" to get a summary of the board details.

Select "Generate Gerber" to download the Gerber files or "Order at JLCPCB" to order the boards direct from JLCPCB. Selecting "Order at JLCPCB" requires you to log in (or create an account and log in).

Wait for the progress bar to complete.

You should now be presented with the image of each side of the board and the following options:

ronoming options:		
Layers	2	
Dimensions	101 x 101 mm	
PCB Qty	10	There is no saving selecting less than 10
PCB Thickness	1.6	
PCB Colour	Green	You may want to change this to Blue
Surface Finish	HASL	
Copper Weight	1 oz	
Gold Fingers	No	
Material Details	FR4-Standard Tg 14	OC
Panel By JLCPCB	No	
Different Design	1	

Note, the price increases significantly if you select a colour other than green.

Select "Save to Cart"

Select "Checkout securely"

Enter your details and select your shipping options.

And finally complete the order.

# History

2018-08-16 2018-09-19	v1.0 v1.1	Circuit board v1.0 manufactured and assembled Circuit board v1.1 manufactured and assembled Modifications made from v1.0 to v1.1: Expansion socket moved slightly Jumpers added between K1/2, K3/4 and K5/6
2018-09-19	e1.1.0	This document updated to edition 1.1.0: Updated photos and minor corrections and additions
2019-03-06	e1.1.1	Revised this user guide General improvements plus corrected some component part numbers

# **Contact Information**

If you wish to contact me regarding this document, or the hardware and software it relates to, use the contact page at <u>www.scc.me.uk</u>

Stephen C Cousins, Chelmsford, Essex, United Kingdom.

#### Modular backplane information

Information about other compatible modular backplane sections can be found at <u>www.scc.me.uk</u>

#### **RC2014 information**

Information about the RC2014 system can be found at www.rc2014.co.uk

#### RC2014 support

Issues related to the RC2014 can be posted on the google group "RC2014-Z80".

## **RC2014** supplies

Parts can be purchased through Tindie at <a href="http://www.tindie.com">www.tindie.com</a> (search "RC2014")

Official RC2014 parts are at: https://www.tindie.com/stores/Semachthemonkey/

# Credits

The design of my first modular backplanes (SC105, SC106 and SC107) was inspired by Jon Langseth's Z50Bus 5 slot backplane, which is available to extend the LiNC80 SBC1. Thanks Jon.

This design (SC113) is the result of improvements made from the earlier designs. In addition these earlier backplanes have provided feedback from other RC2014 users. So thanks to those who have provided valuable feedback and comments.

A further influence has been Tom Szolyga. I've recently been discussing many aspects of retro computers with Tom and we have been actively working towards some common designs, including modular backplanes. This design (SC113) is one of the first products to (hopefully) meet the specification we've been working on. Thanks Tom.